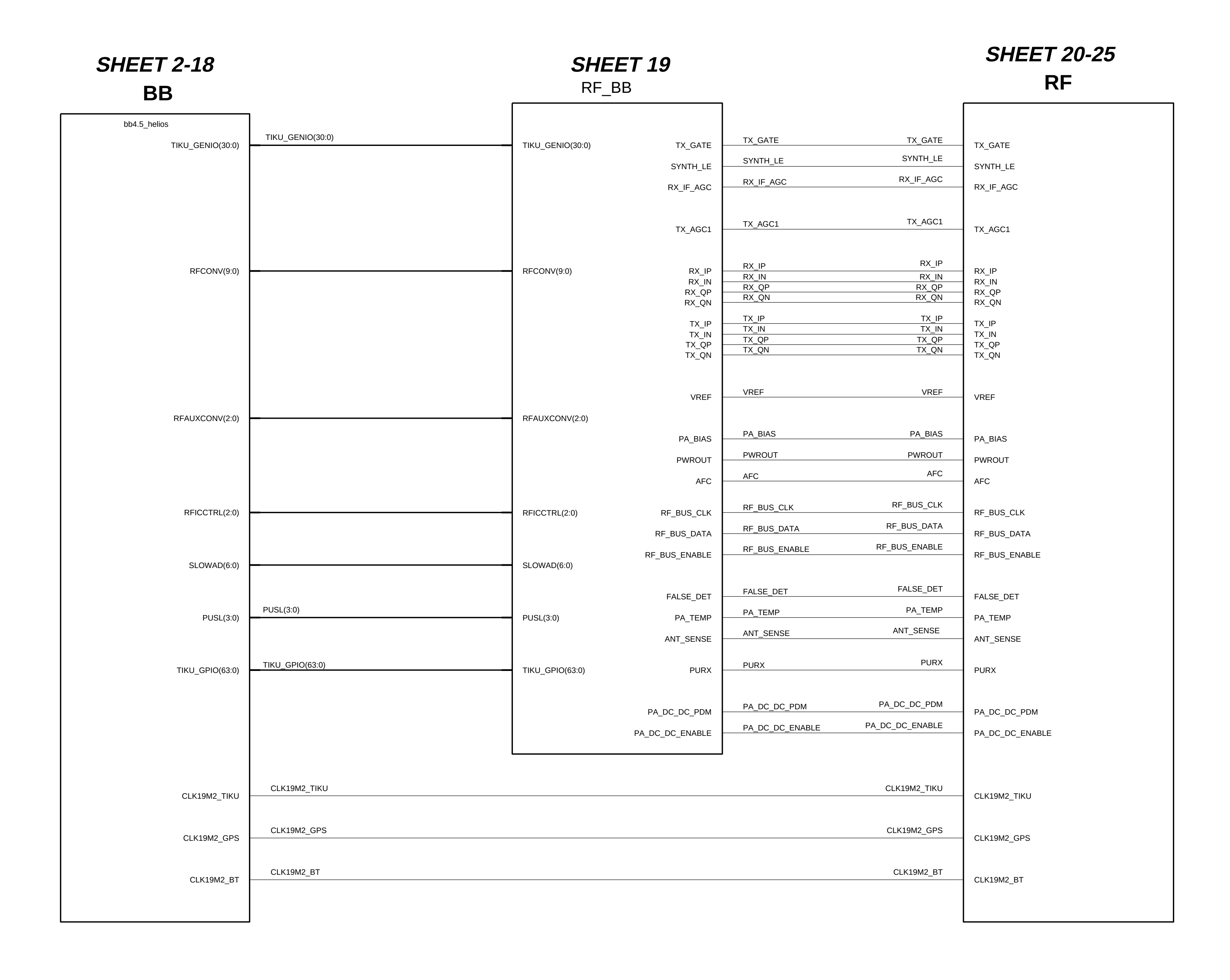
Schematics - Main PWB

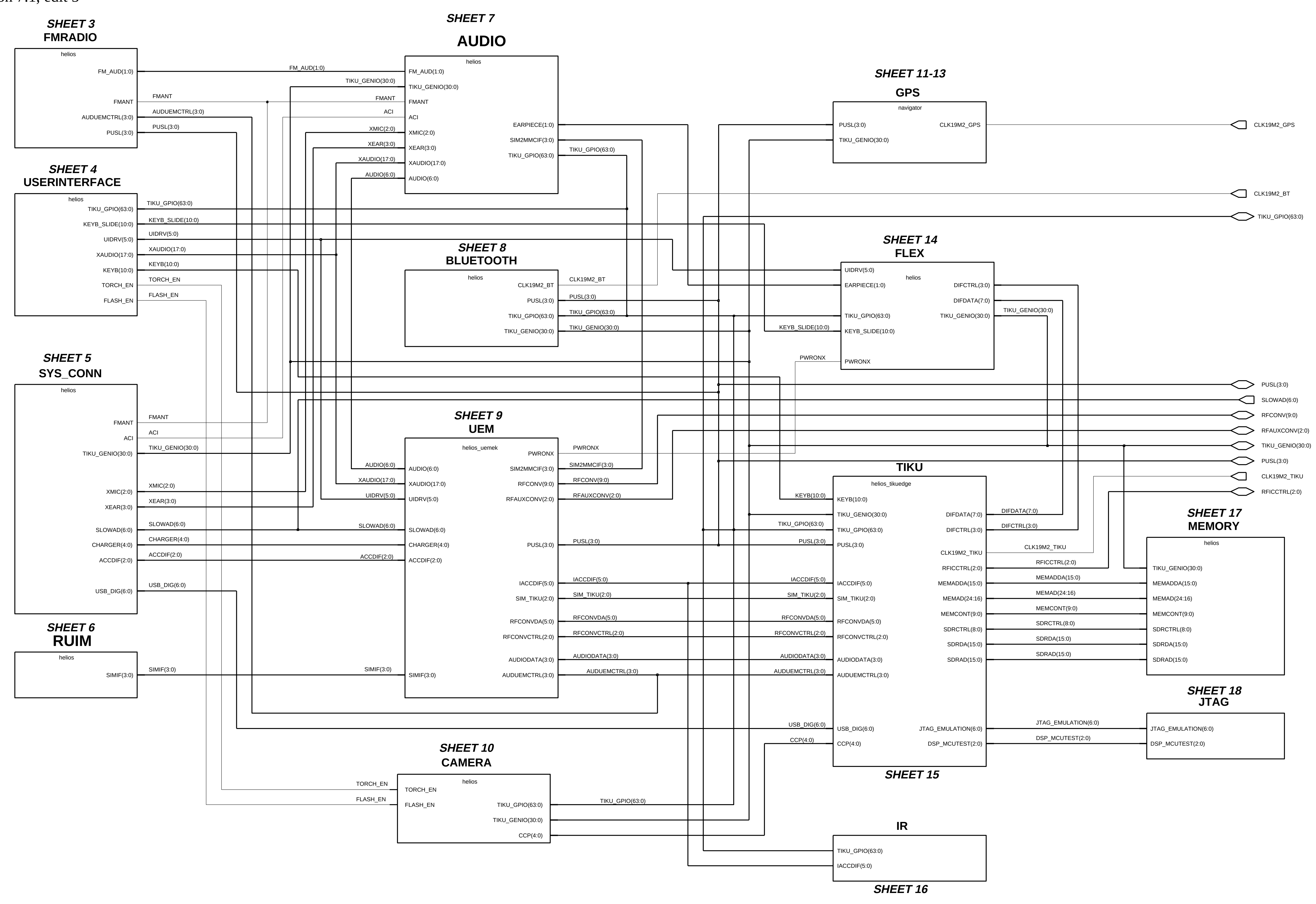
Top Level

Version 7.1, edit 5



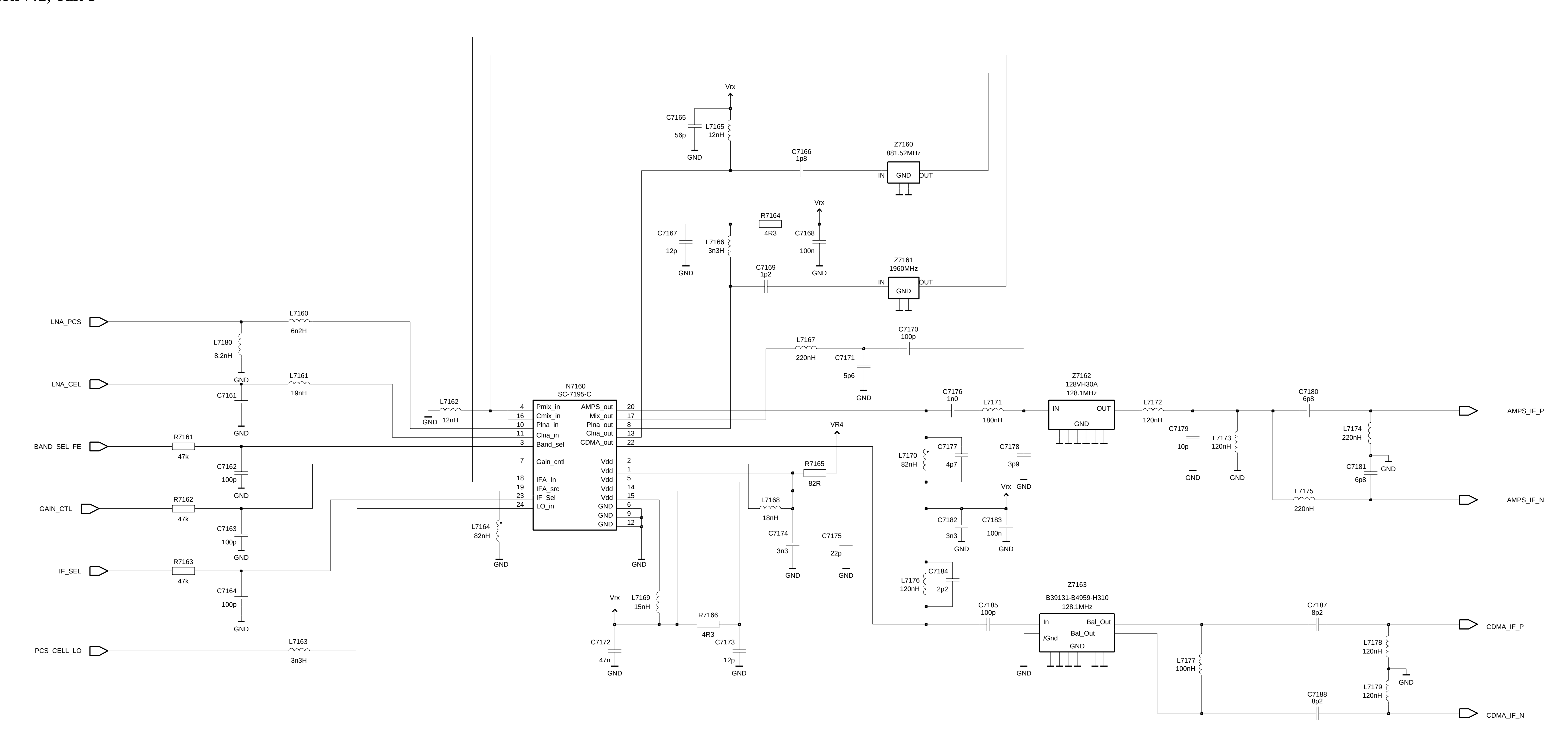
Baseband - Top Level

Version 7.1, edit 5



Front End

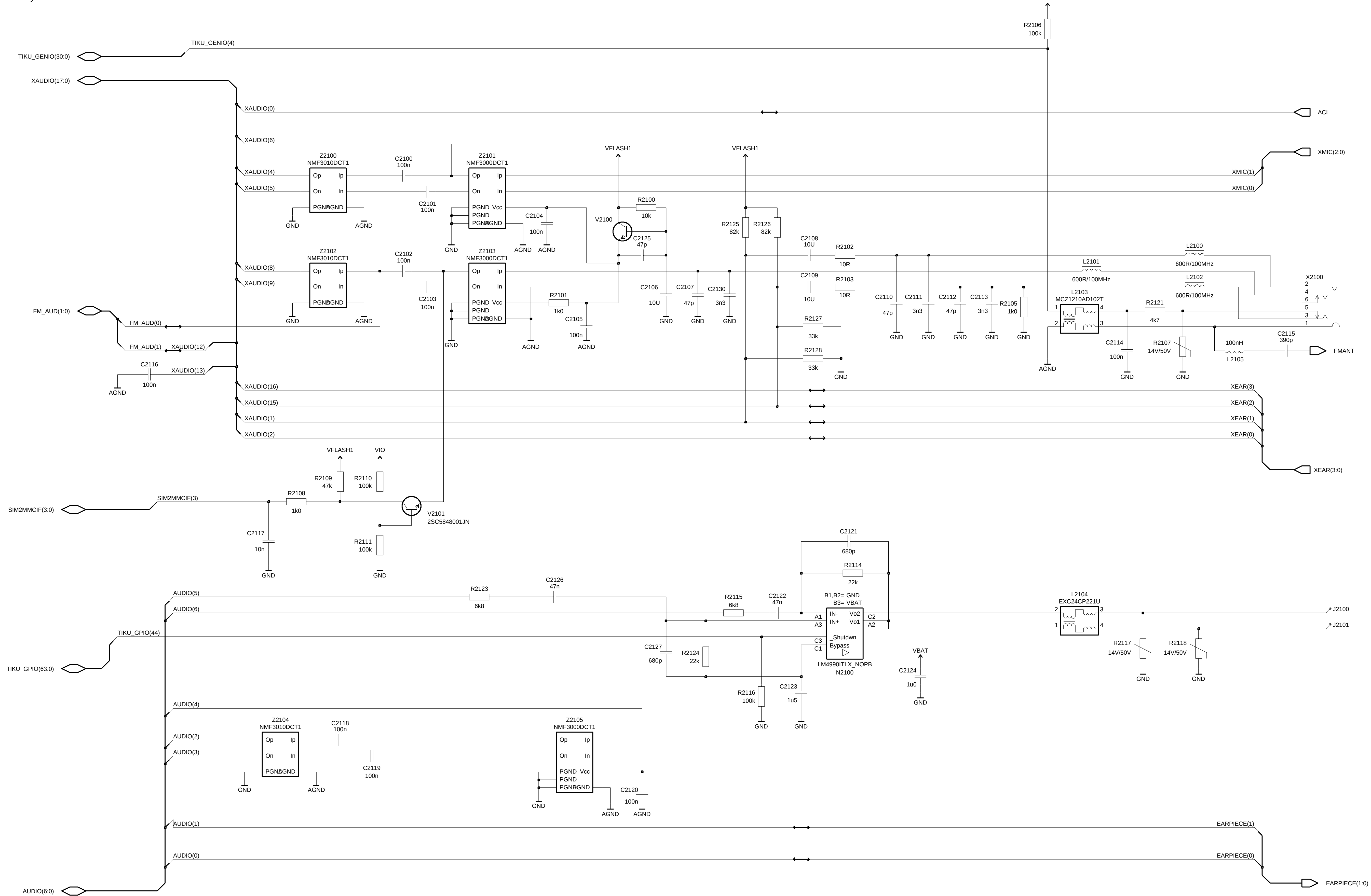
Version 7.1, edit 5



	LOGIC INPUTS		
MODES	BAND	GAIN_CTL	IF_SEL
CEL CDMA Hi Gain	0	1	0
CEL CDMA Lo Gain	0	0	0
PCS CDMA Hi Gain	1	1	0
PCS CDMA Lo Gain	1	0	0
AMPS Hi Gain	0	1	1
AMPS Lo Gain	0	0	1

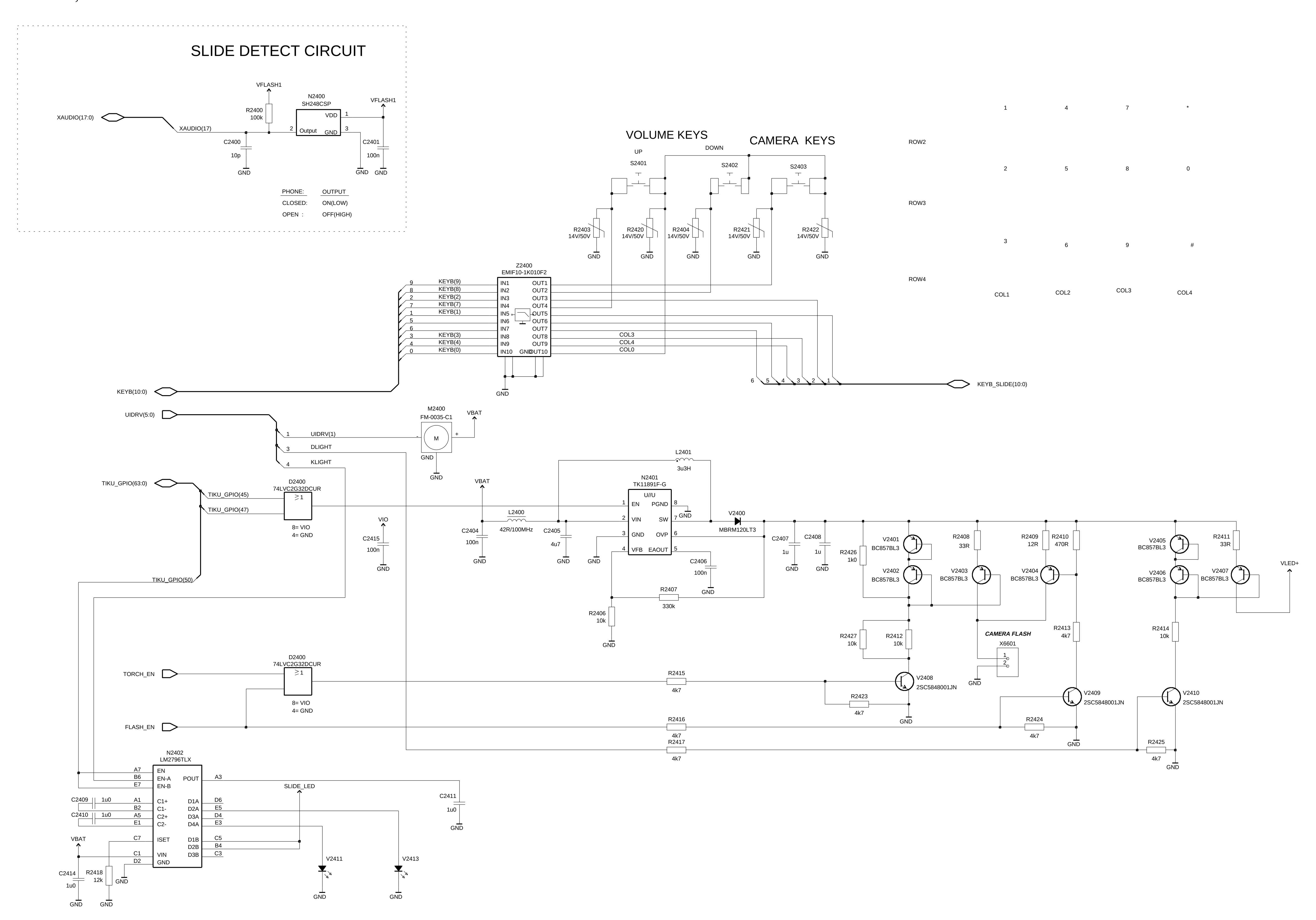
Audio

Version 7.1, edit 5



UI

Version 7.1, edit 5



Memory

Version 7.1, edit 5

D3000
KAK13V006B-DUUY
COMBO 8Mx16 NOR
16Mx16 NAND
8Mx16 SDRAM

SDRAD(15:0)

Net	Pin
SDRAD(0)	P10
SDRAD(1)	M10
SDRAD(2)	N10
SDRAD(3)	M3
SDRAD(4)	P4
SDRAD(5)	N4
SDRAD(6)	M4
SDRAD(7)	P5
SDRAD(8)	N5
SDRAD(9)	M5
SDRAD(10)	M9
SDRAD(11)	N9
12	N11
13	M11
	N12

A, MSDRAM

NOTE:
SDCTRL(8) MUST BE TIED LOW ON TIKU SHEET.

SDRCTRL(8:0)

Net	Pin	Function
SDRCTRL(1)	M6	CKE
SDRCTRL(0) (R3000 47R)	P7	CLKd
SDRCTRL(8)	L3	_CEd
SDRCTRL(2)	P6	_RAS
SDRCTRL(3)	N6	_CAS
SDRCTRL(4)	M8	_WEd
SDRAD(12)	P9	BA0
SDRAD(13)	N8	BA1
SDRCTRL(5)	B9	LDQM
SDRCTRL(6)	C6	UDQM

MEMAD(24:16)

Net	Pin
MEMAD(16)	J1
MEMAD(17)	E3
MEMAD(18)	F3
MEMAD(19)	F2
MEMAD(20)	L1
MEMAD(21)	K1
MEMAD(22)	E2
MEMAD(23)	F1
	E1
	D1

A, NOR

VIO, R3001 100k

Net	Pin	Function
MEMCONT(7)	G1	_CEnor
	G2	_WP
28	J3	INT
MEMCONT(2)	M12	_CEmn
MEMCONT(1)	D12	_OE
MEMCONT(0)	G3	_WE
MEMCONT(6)	H3	CLK
MEMCONT(8)	M13	RDY
MEMCONT(5)	K2	_AVD
MEMCONT(9)	L2	_Reset

NAND, Common

DNU: A1, A2, A13, A14, B1, B14, N1, N14, P1, P2, P13, P14
NC: B4, C11, D2, D3, M1, N7, P3

TIKU_GENIO(30:0)
MEMCONT(9:0)

MEMADDA(15:0) — Address/Data I/O Common

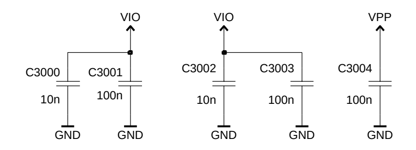
Net	Pin
MEMADDA(0)	E14
MEMADDA(1)	E13
MEMADDA(2)	F14
MEMADDA(3)	F13
MEMADDA(4)	H14
MEMADDA(5)	J14
MEMADDA(6)	K14
MEMADDA(7)	K13
MEMADDA(8)	D13
MEMADDA(9)	E12
MEMADDA(10)	G14
MEMADDA(11)	G13
MEMADDA(12)	H13
MEMADDA(13)	H12
MEMADDA(14)	L14
MEMADDA(15)	L13

SDRDA(15:0) — Data I/O MSDRAM

Net	Pin
SDRDA(0)	C13
SDRDA(1)	B12
SDRDA(2)	C12
SDRDA(3)	A11
SDRDA(4)	B11
SDRDA(5)	A10
SDRDA(6)	B10
SDRDA(7)	C10
SDRDA(8)	A6
SDRDA(9)	A5
SDRDA(10)	B5
SDRDA(11)	C5
SDRDA(12)	C4
SDRDA(13)	B3
SDRDA(14)	C3
SDRDA(15)	C2

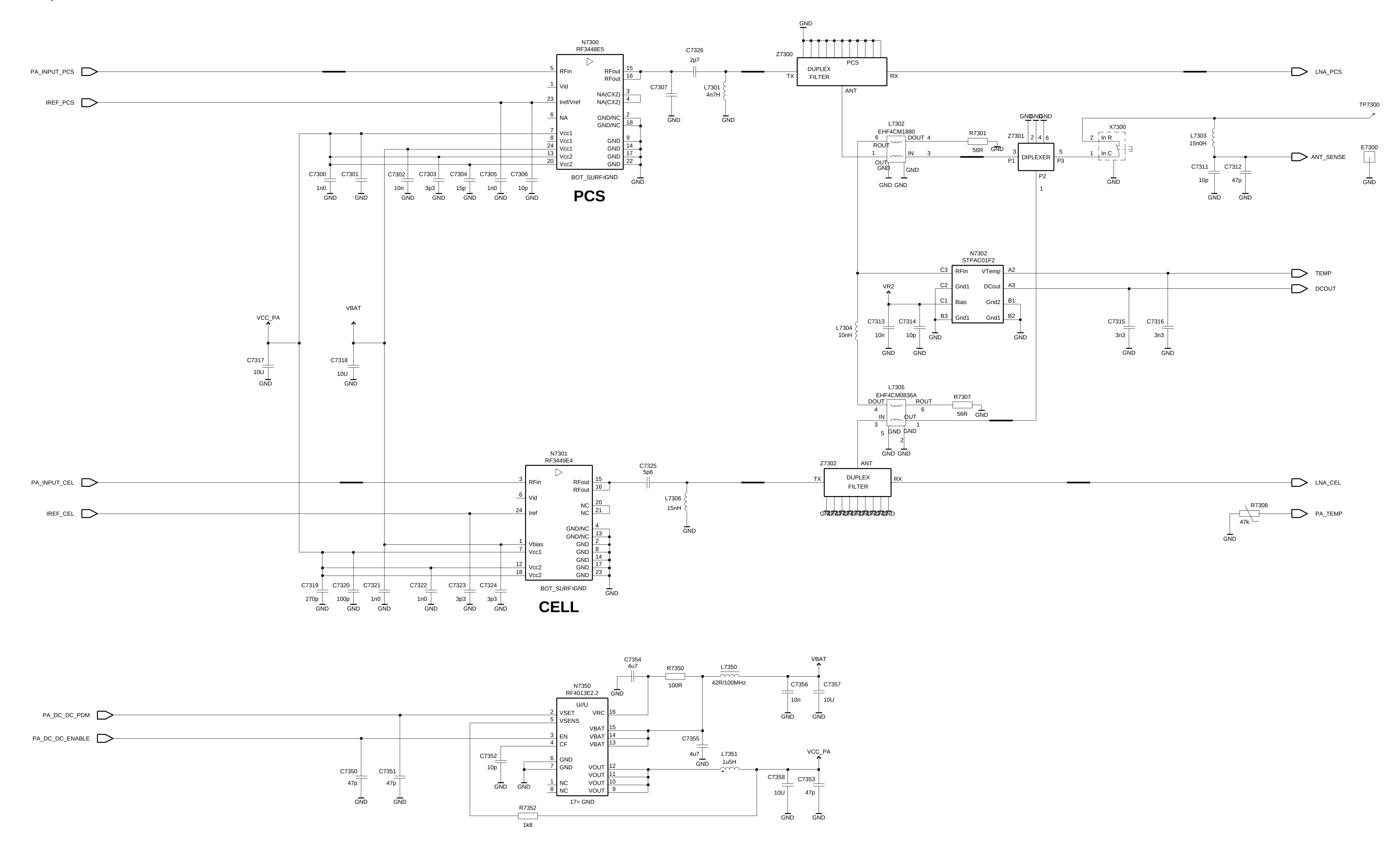
A8,A9,C1,C8,D14,P8,P11,N3= VIO
A4,B7,B8,B13= VIO
H2,N13= VIO
K3,K12= VIO
G12,L12= VIO
H1= VPP
A7,B2,C7,C14,M7,N2,P12= GND
A3,A12,B6,C9= GND
F12,J2,J12,J13,M2,M14= GND

VIO VPP
R3002
4k7



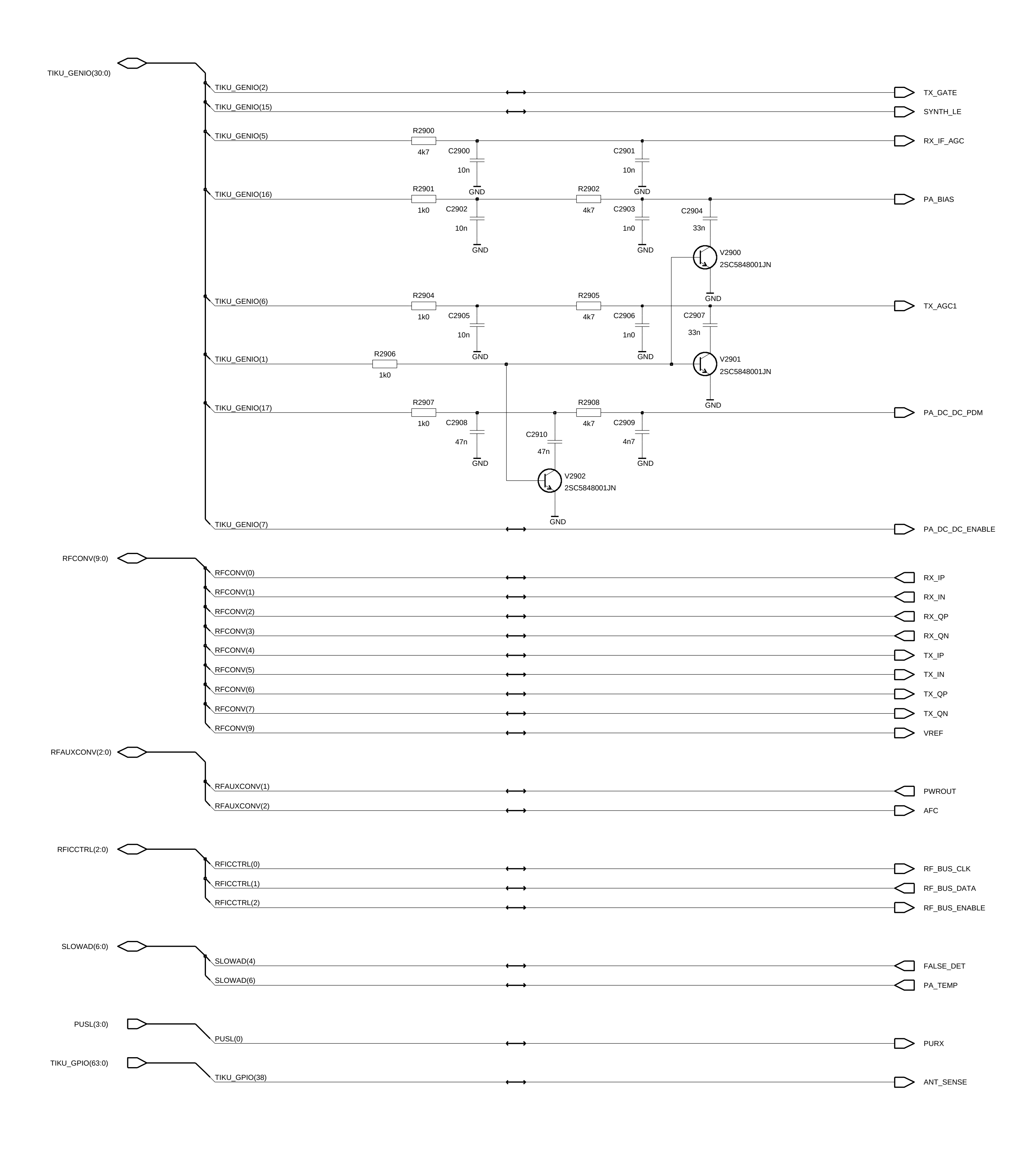
PA

Version 7.1, edit 6



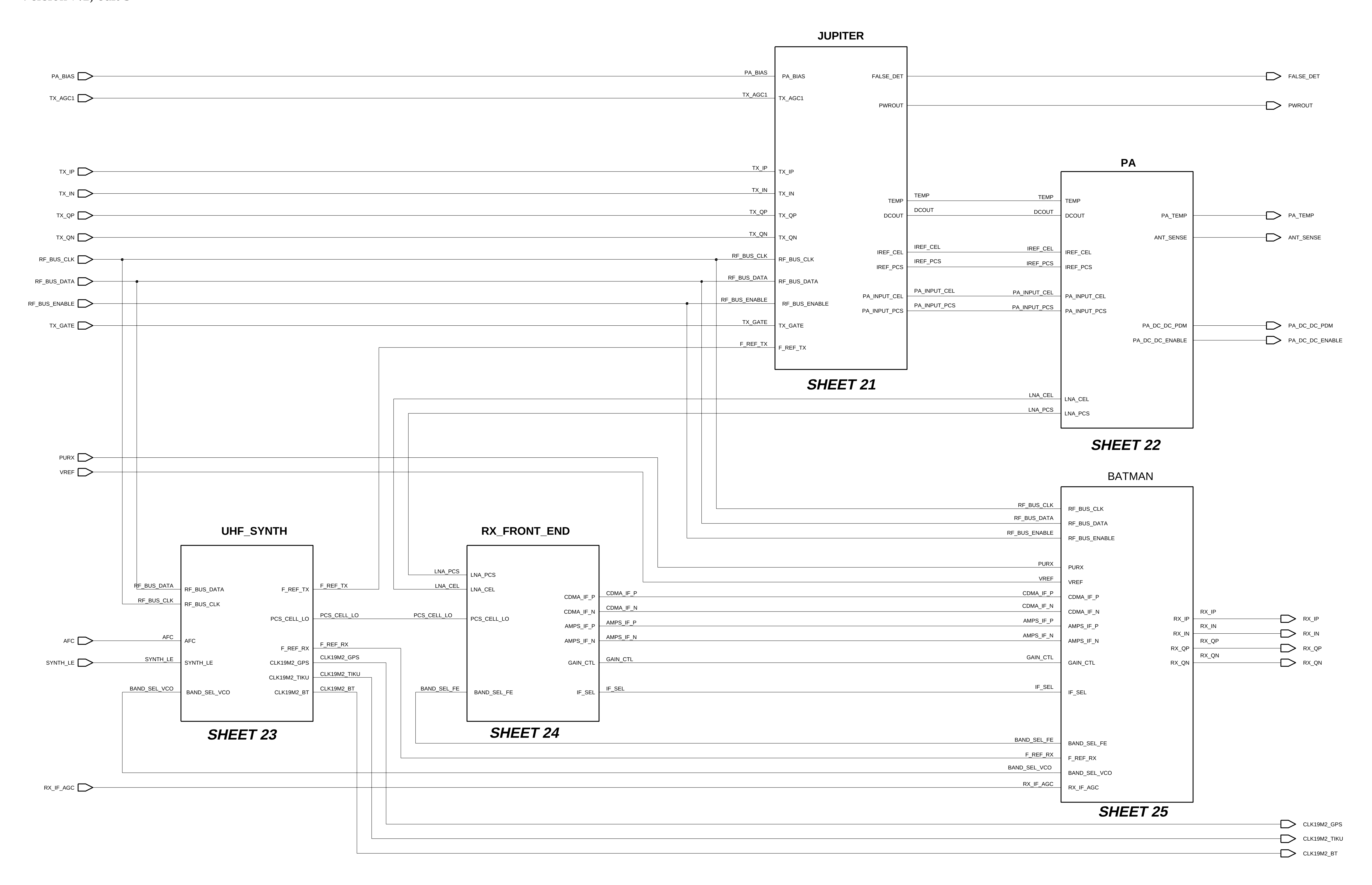
RF-BB Interface

Version 7.1, edit 5



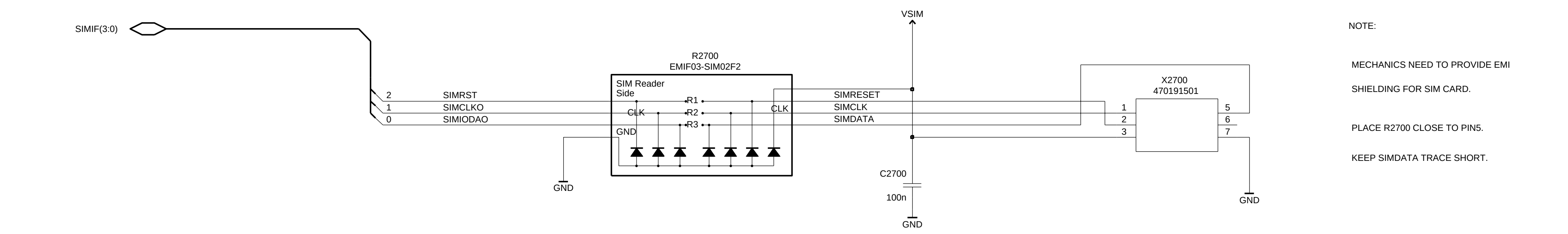
RF - Top

Version 7.1, edit 5



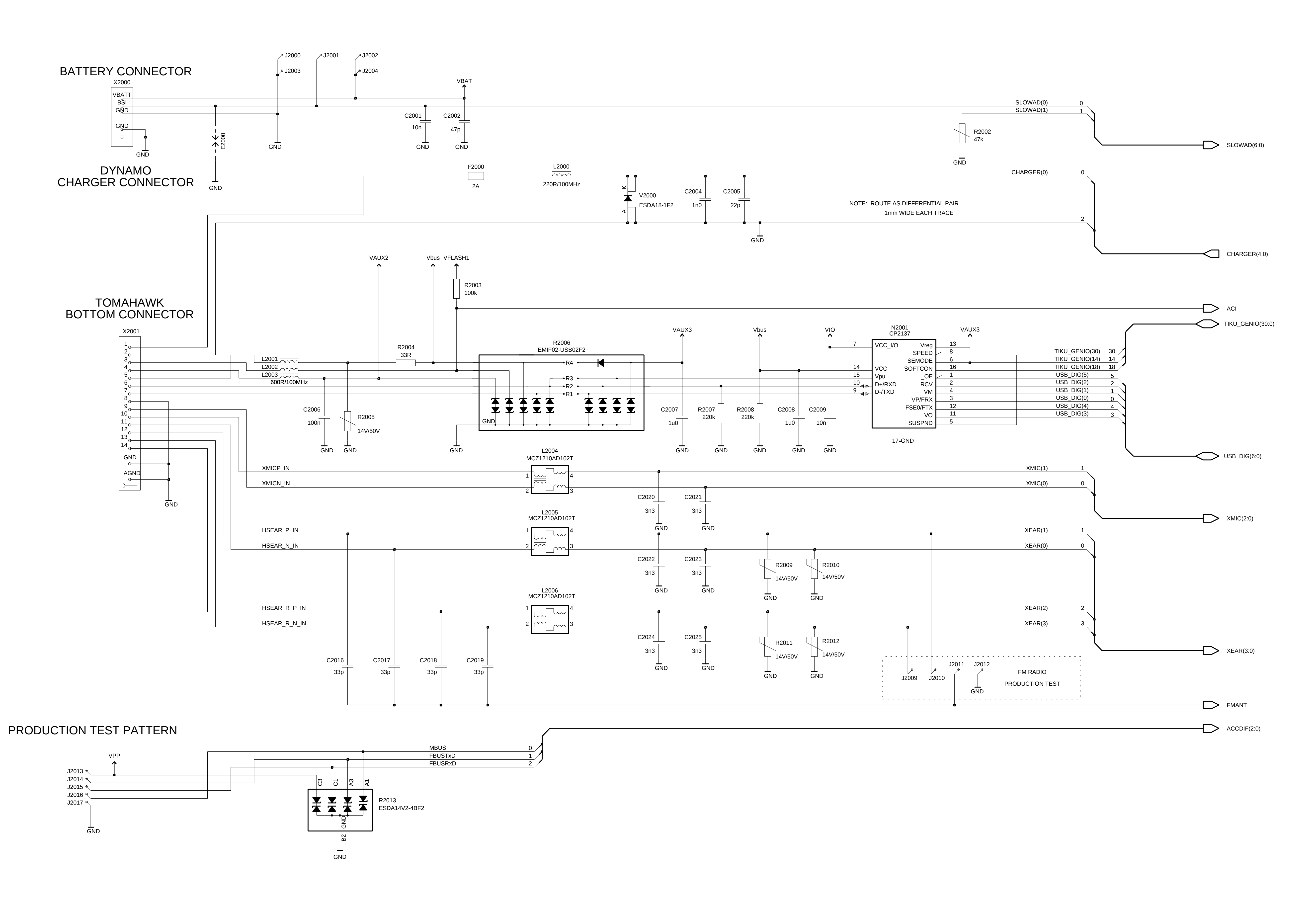
UIM Connector

Version 7.1, edit 5



System Connector

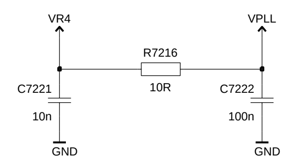
Version 7.1, edit 5



UHF Synthesizer

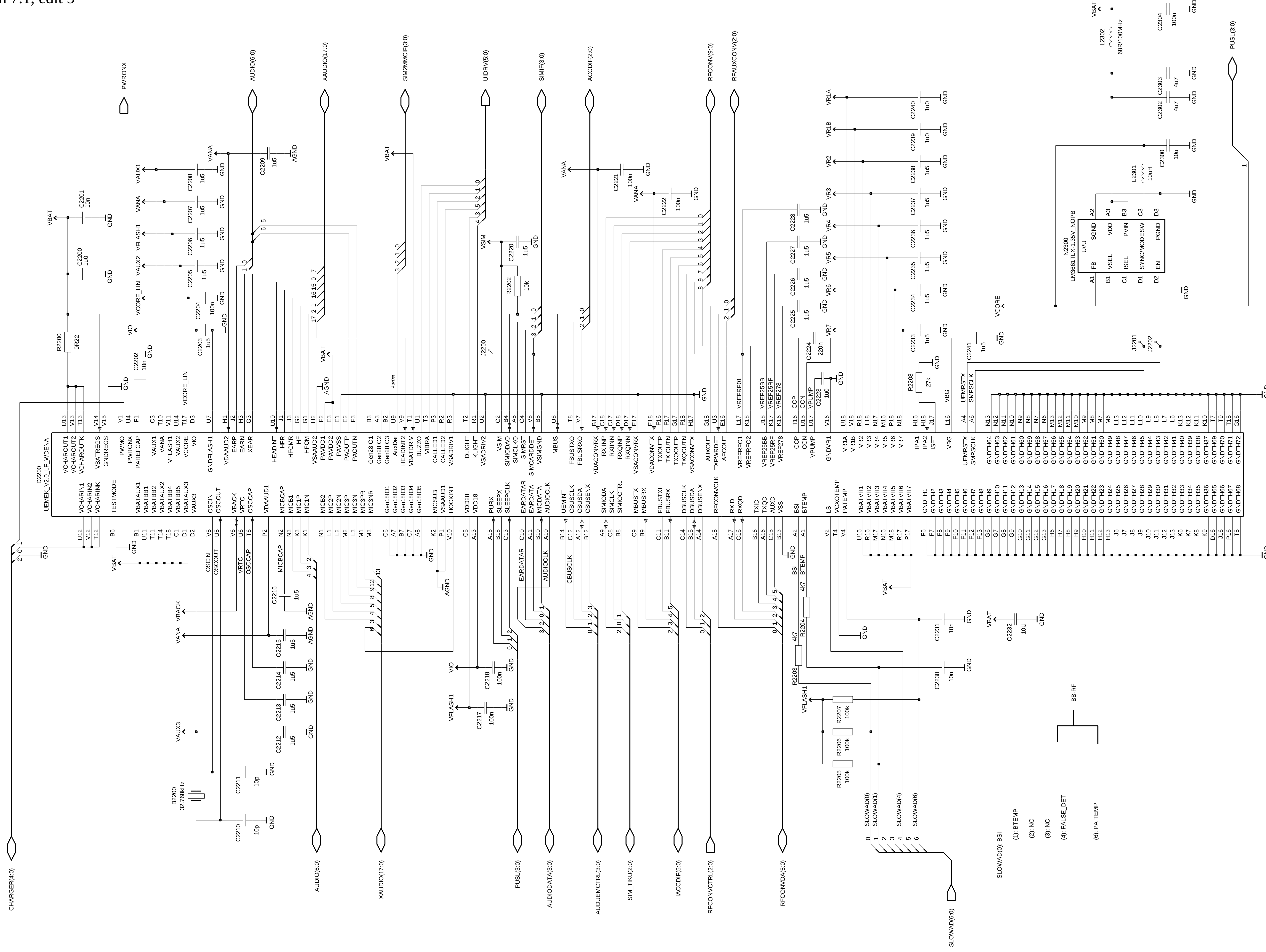
Version 7.1, edit 5

VR3
R7200 10R
C7200 1u0
GND
C7201 10n
GND
G7200 +DC
AFC
R7201 10k
VC NKG3178B OUT
19.2MHz GND
C7202 1n0
GND
GND
C7203 470p
R7206 100R
CLK19M2_TIKU
C7205 470p
CLK19M2_GPS
C7204 100p
CLK19M2_BT
C7206 470p
F_REF_TX
C7207 470p
F_REF_RX
VR4
C7218 1u0
C7219 10n
GND
SYNTH_LE
RF_BUS_DATA
RF_BUS_CLK
SYN01
SYN02
SYN03
N7200
LMX2310USLDX
13 LE
12 Data
10 Clock
C7208 470p
6 OSCin OSCout 8
20 Vp CPo 2
16 Vuc
5 Finb FoLD 9
4 Fin FL 19
15 CE
18= VPLL
3,14,= GND
VR1A
R7209 10R
C7209 10n
GND
C7210 100n
GND
C7211 47p
GND
VIO
R7210 150R
C7213 3n3
GND
R7211 3k9
C7214 33n
GND
C7215 6n8
GND
R7212 10k
C7216 1n0
GND
R7213 10k
C7217 1n0
GND
G7201 VCC
VT ENFVJ5A2F10 BS OUT
GND
L7201 3n3H
PCS_CELL_LO
C7212 1p8
BAND_SEL_VCO
R7215 2k2
C7220 100n



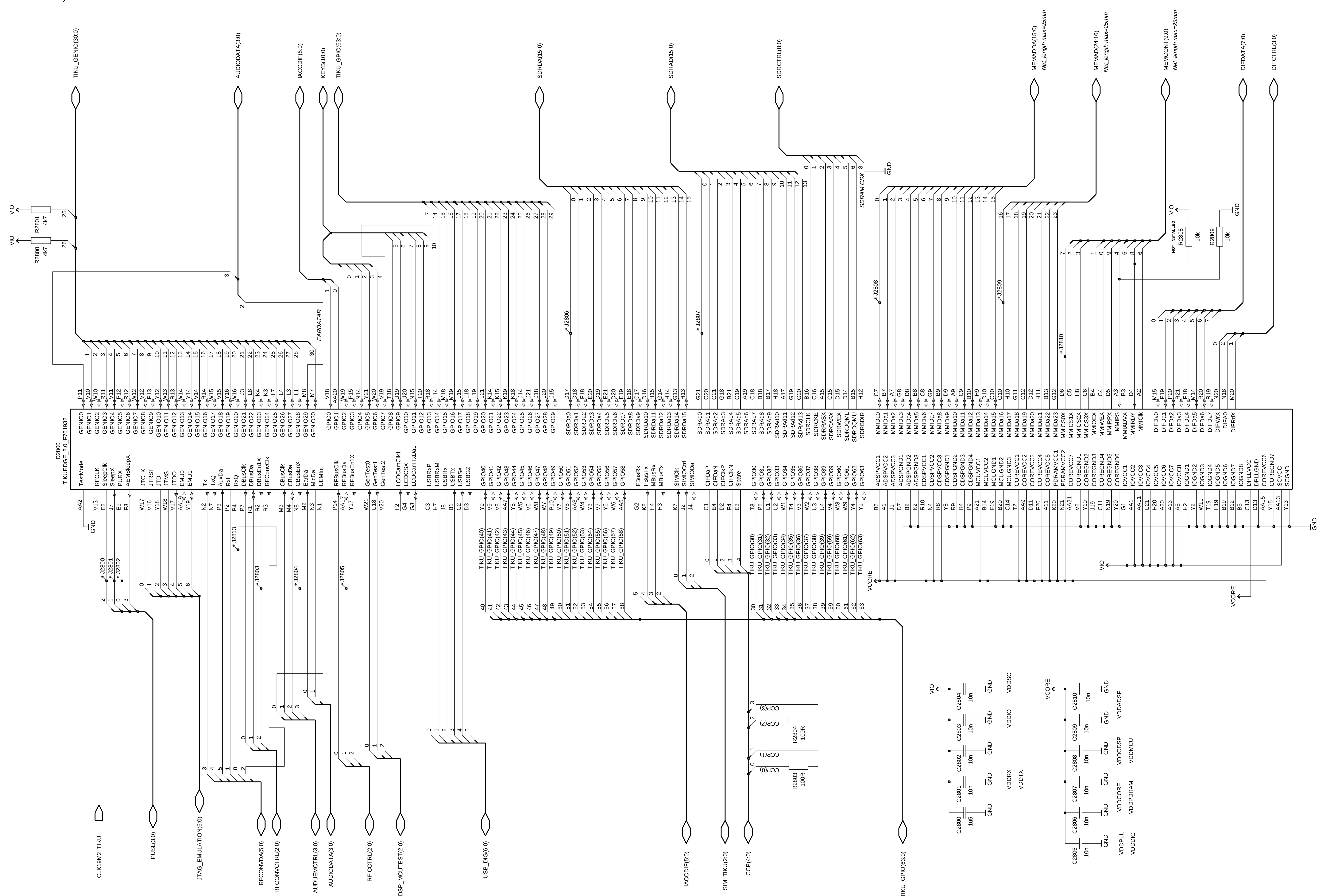
UEME

Version 7.1, edit 5



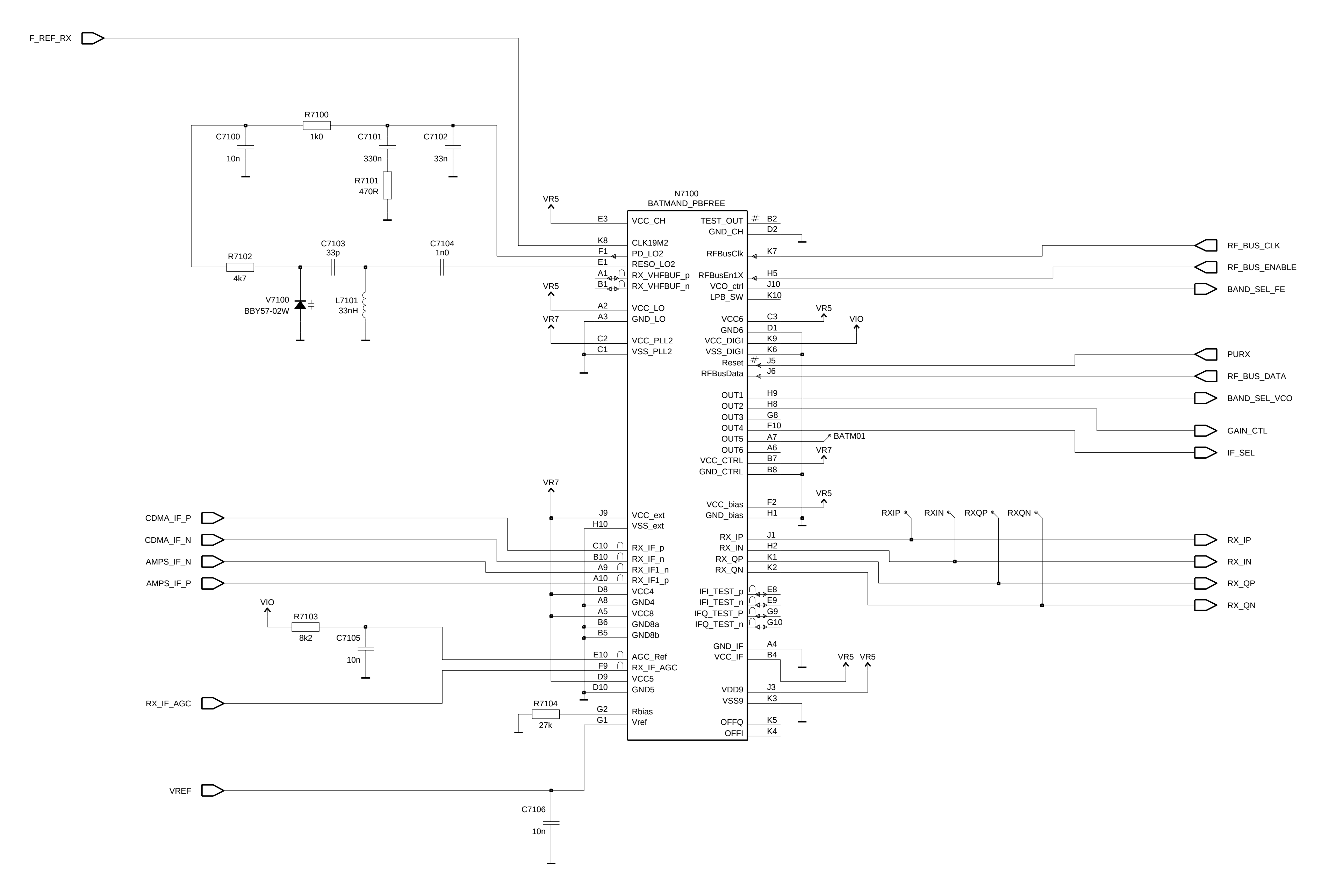
UPP

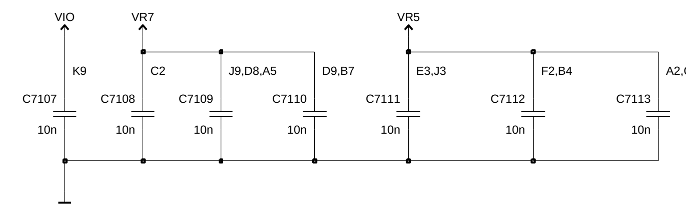
Version 7.1, edit 5



Rx

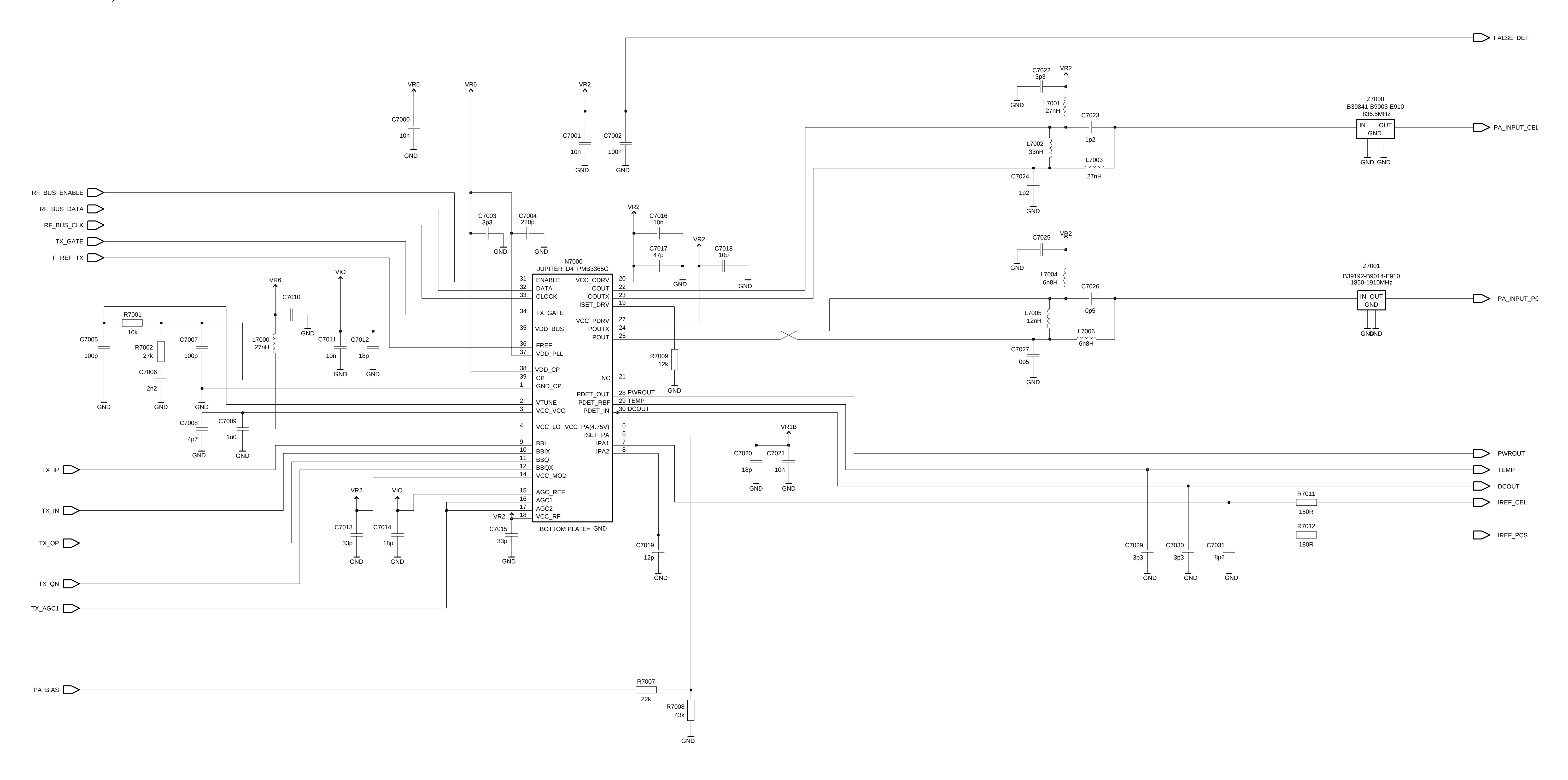
Version 7.1, edit 5





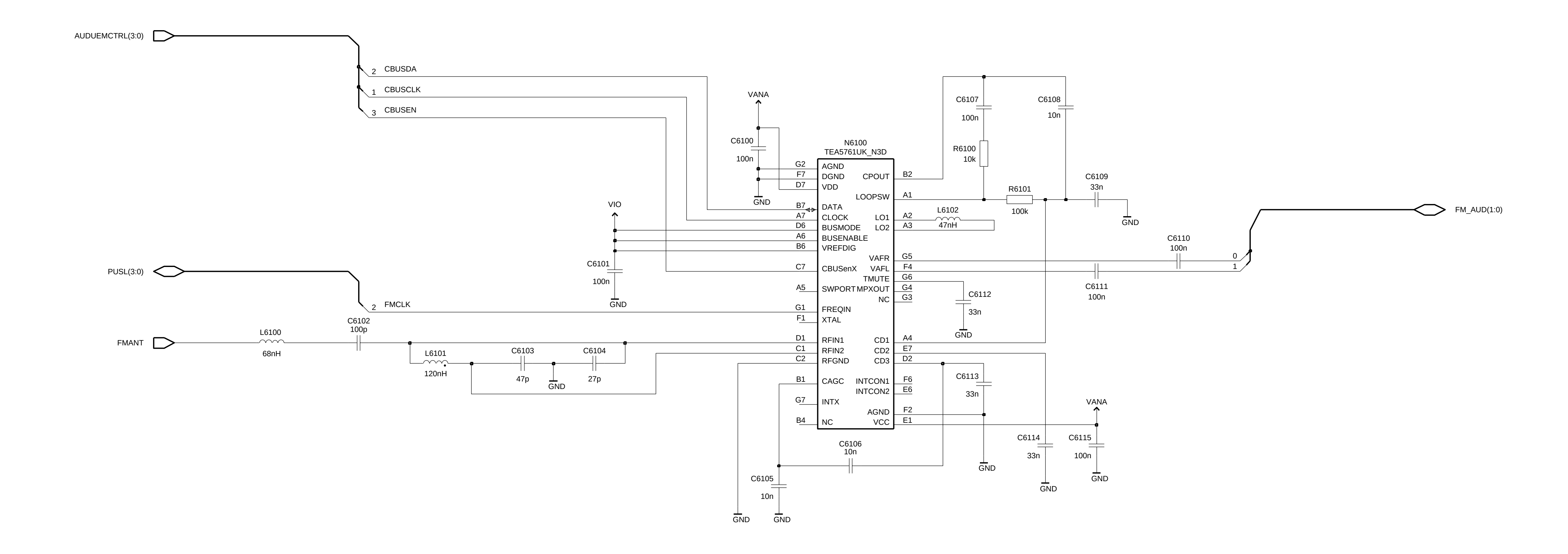
Tx

Version 7.1, edit 5



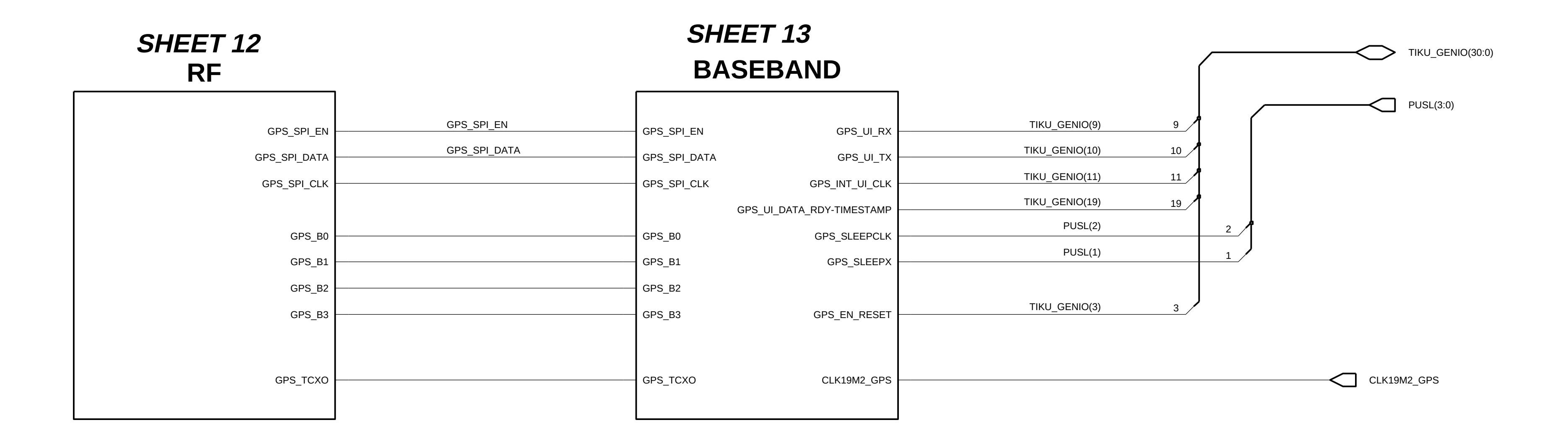
FM Radio

Version 7.1, edit 5



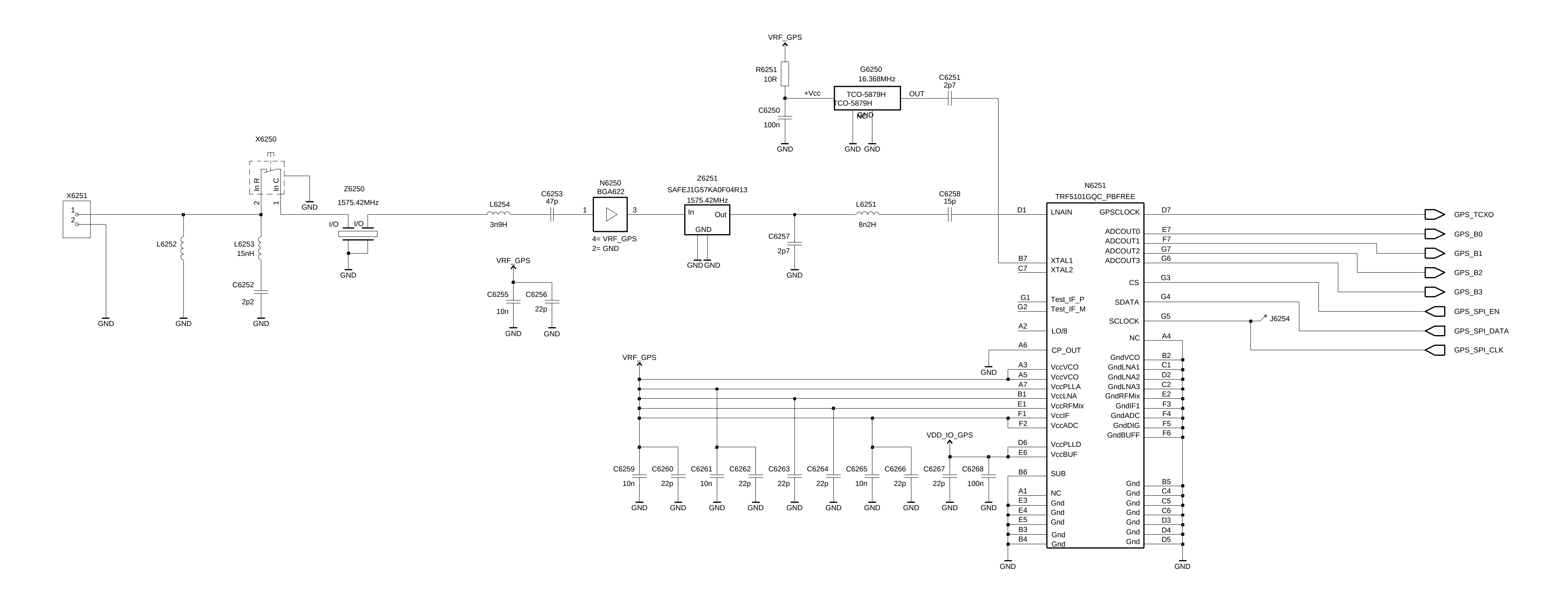
GPS Top Level

Version 7.1, edit 5



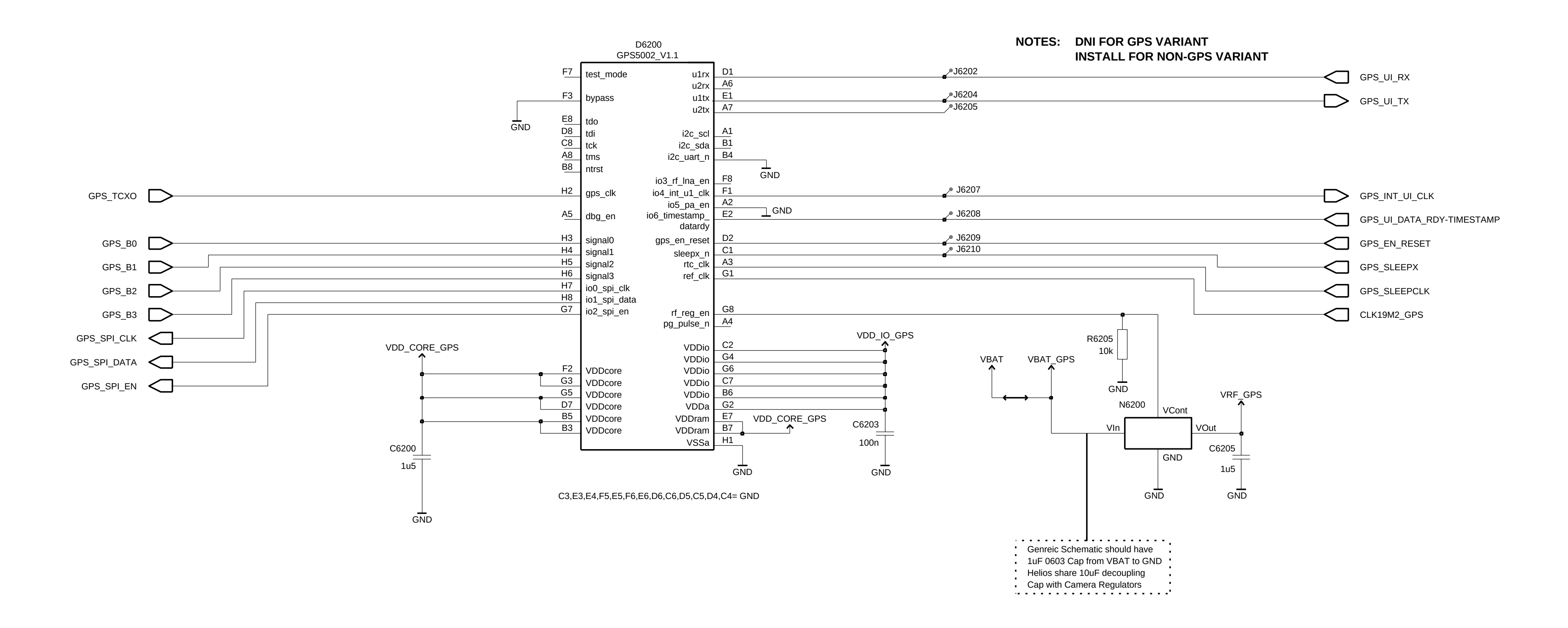
GPS RF

Version 7.1, edit 5



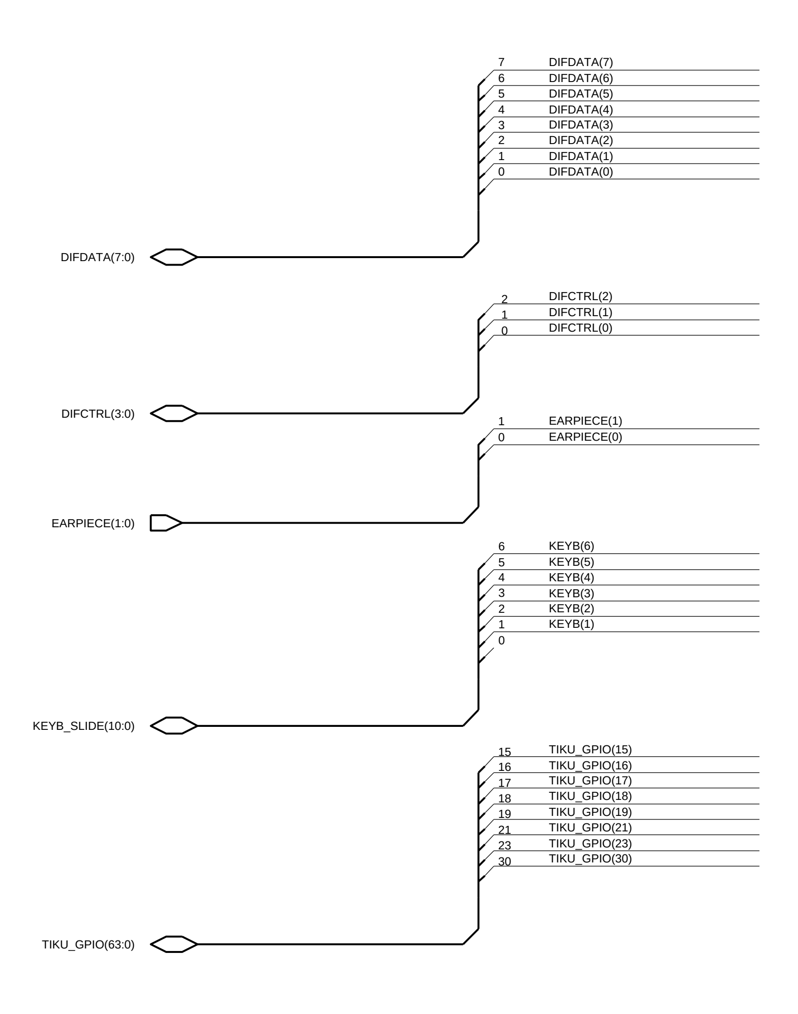
GPS BB

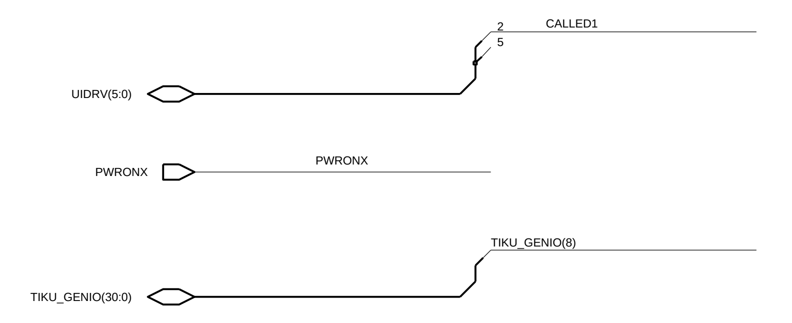
Version 7.1, edit 5

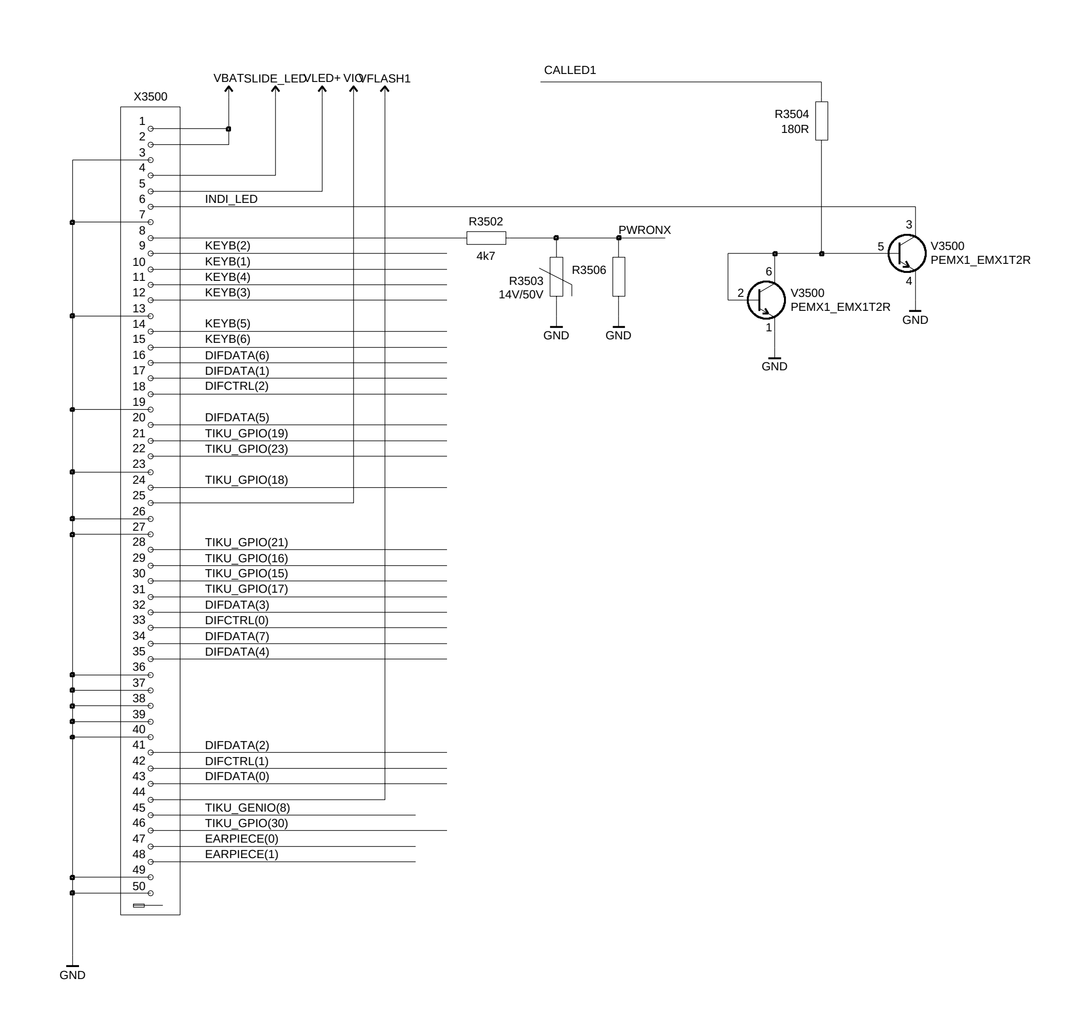


Flex Connector

Version 7.1, edit 5

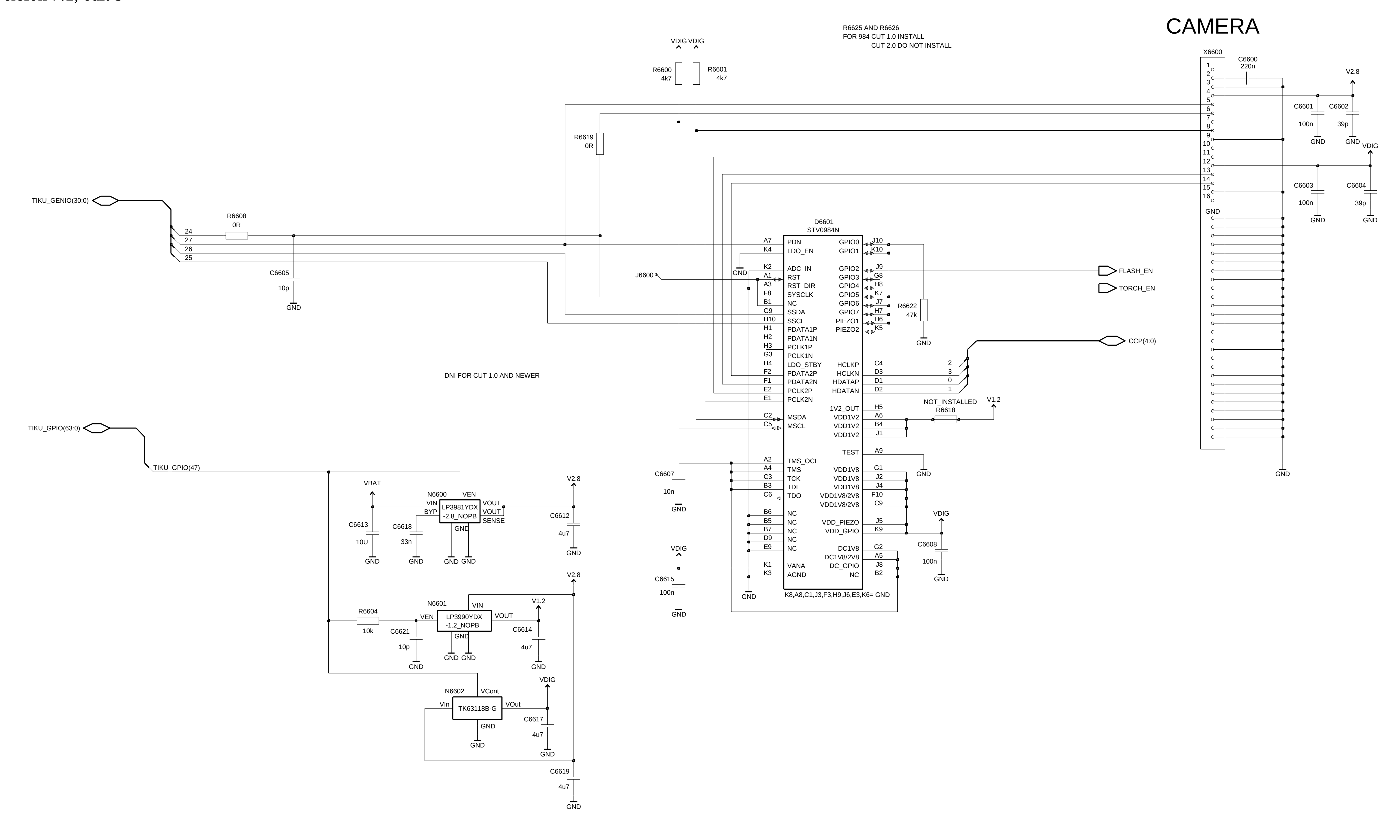






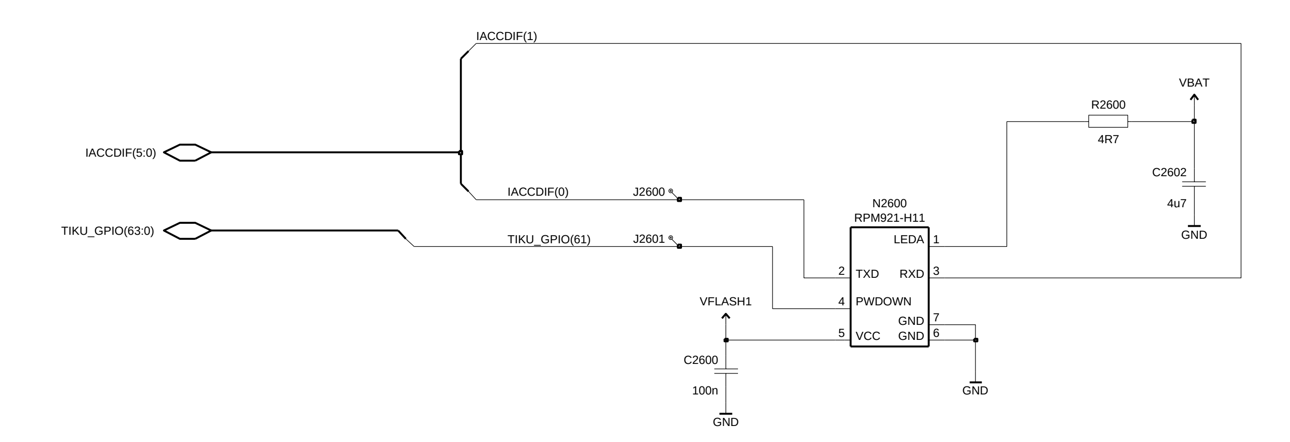
Camera and Flash

Version 7.1, edit 5



Infrared

Version 7.1, edit 5



Bluetooth

Version 7.1, edit 5

TIKU_GENIO(30:0)
TIKU_GPIO(63:0)
PUSL(3:0)
CLK19M2_BT

N6000
PBRF6150DZSLR

21 UART_RX E4 RX
22 UART_CTS E8 CTS
27 PCM_IN R6000 47R C5 AUD_DI
29 PCM_SYNC A7 AUD_FSYNC
26 PCM_CLK B6 AUD_CLK
B5 TMS
D4 TCK
C3 TDI
H1 RFP
H2 RFM
B2 ANATST1
C1 ANATST2
F6 SLOW_CLK
E1 FREFM
13 BT_WAKEUP C8 BT_WAKEUP
D6 EMRST
G5 VLDO_OUT
F5 BBLDO_IN
G6 KA_OUT
20 BT_RESETX E3 EXTEN
H6 VBAT
G7 VDDS3R
D8 VDDS18
A3 VDDS3L
H8 VDDS3R_F
B3 VDDS3L_F
F8 VDD
H5 VDD
A4 VDD
D2 VDDA
F2 VDDA1
F1 VDDA4
B1 VDDA5
A1 VDDA6
C2 VDDA8
H3 VDDA9
G3 VDDA10
G4 VDDA11

TX E5 UART_TX 20
RTS E7 UART_RTS 23
AUD_DO C7 PCM_OUT 28
TDO E6
TX_DBG A8
FREFP E2
HOST_WAKEUP D7 HOST_WAKEUP 12
IO1 C6
IO2 B7
IO3 D5
IO4 A6
IO5 C4
IO14 B4
IO15 F7
VSS B8
VSS G8
VSS H7
VSS A5
VSS A2
HVM_VSS F4
VSSA H4
VSSA D1
VSSA1 G1
VSSA1 G2
VSSRF F3
GND

Z6000
LFB2B2G44BB2A285
Unbal_port
DC
GND
Bal
Bal
2441.75MHz

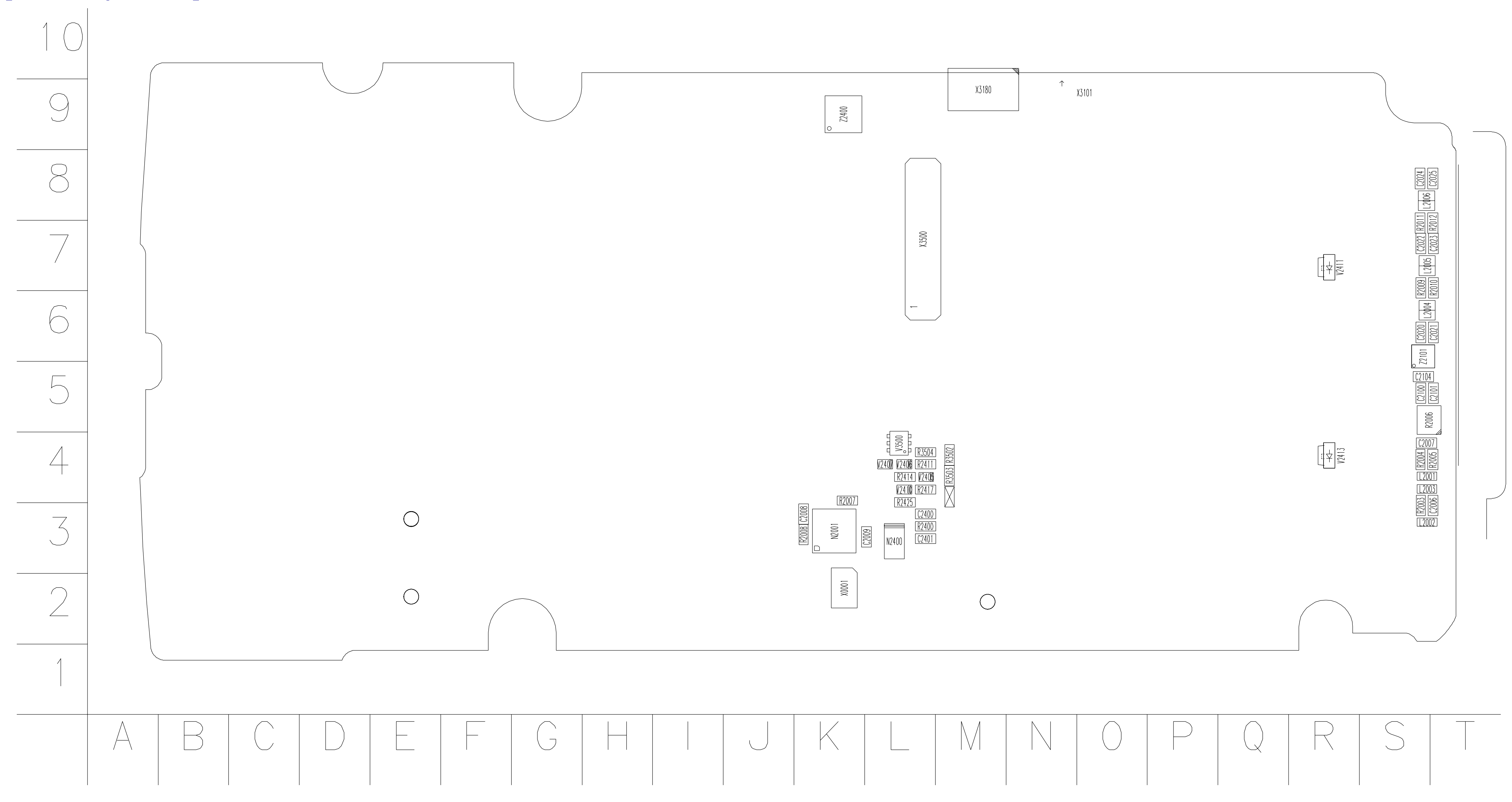
L6001
2n2H

J7002
J7003
BT ANTENNASTRIP
GND
L6000
22nH
C6000
15p
GND
GND

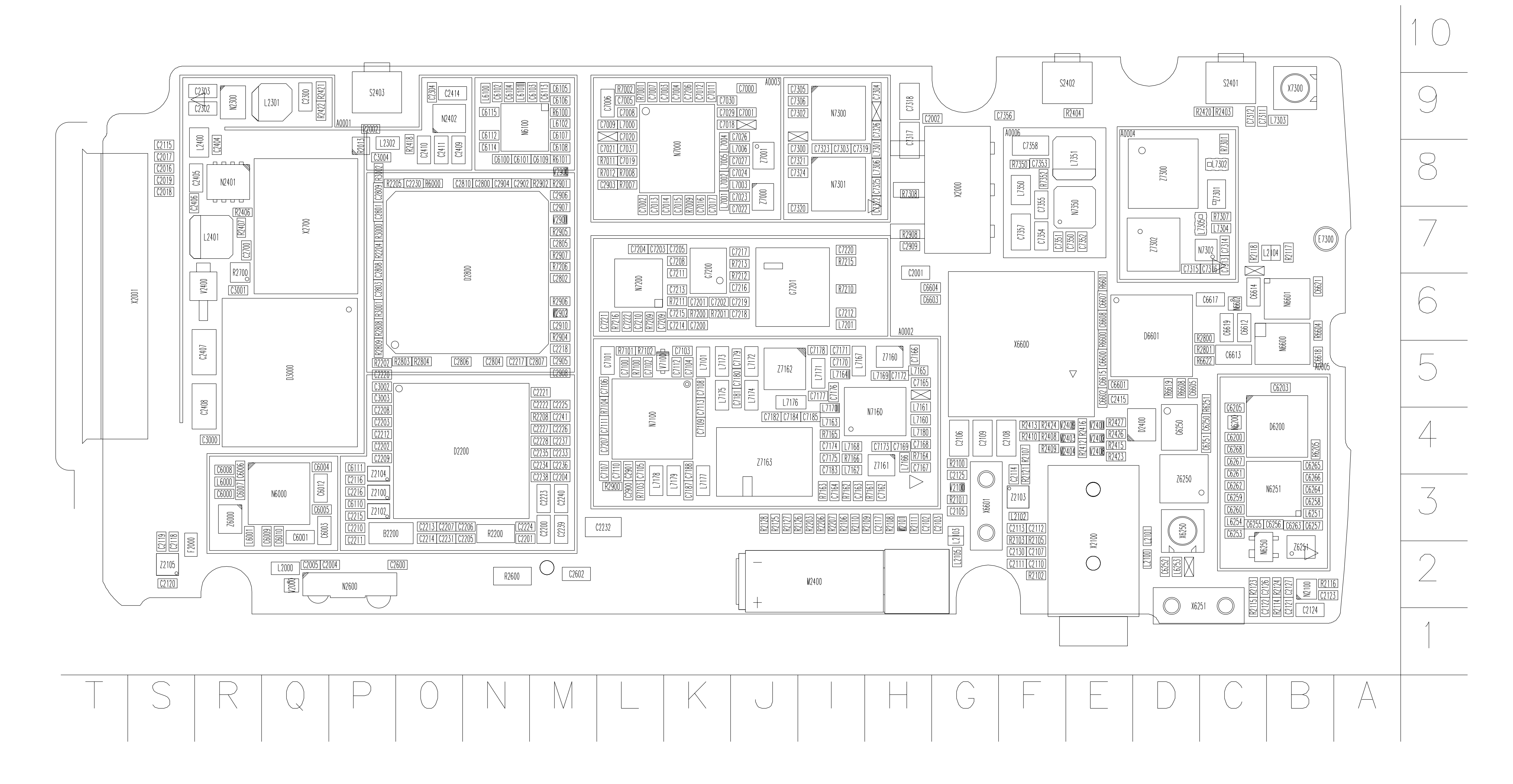
VIO
VBAT
C6012
4u7
GND
C6001
4u7
GND

C6003 4u7 GND
C6004 1u0 GND
C6005 1u0 GND
C6006 100n GND
C6007 100n GND
C6008 0u47 GND
C6009 1u0 GND
C6010 0u47 GND

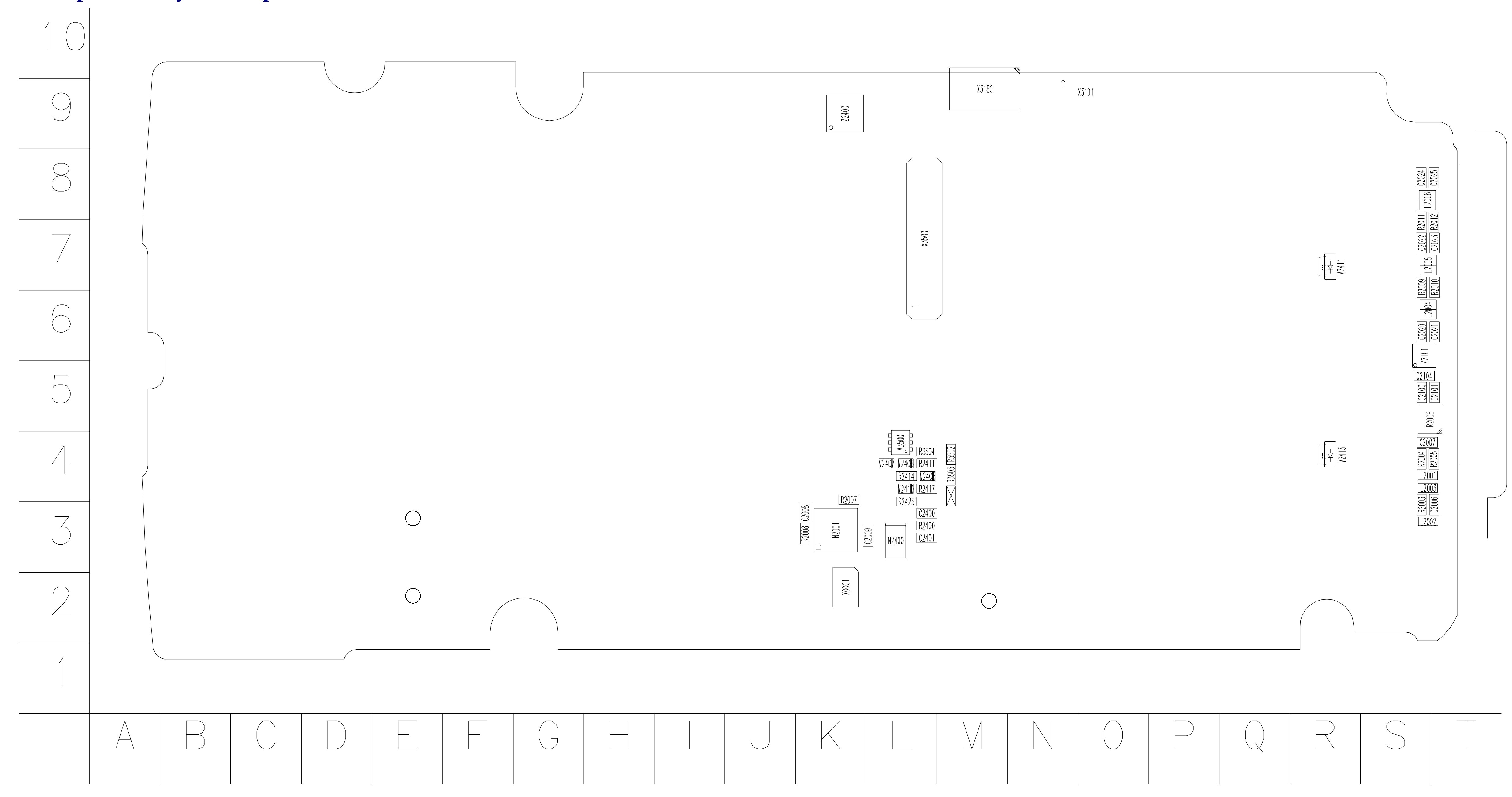
6265i Component Layout - Top



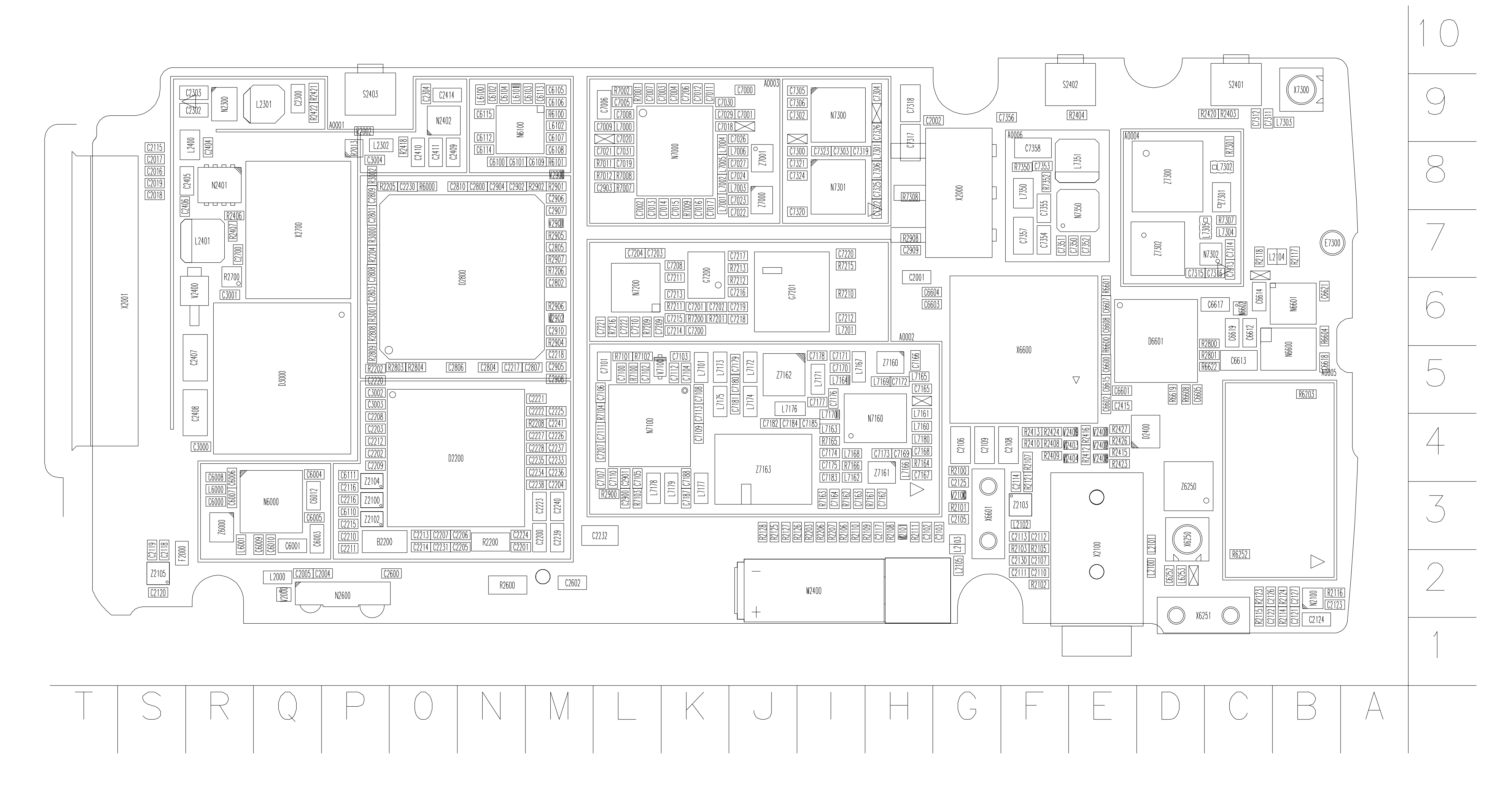
6265i Component Layout - Bottom



6265/6268 Component Layout - Top



6265/6268 Component Layout - Bottom



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